



ATTY. DOCKET NO.

2380-788

SERIAL NO.

10/629,773

APPLICANT

HÖRLIN et al

FILING DATE

July 30, 2003

GROUP

-2661 2663

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
RD	5,721,833	2-1998	Cooperman et al.			
	5,862,128	1-1999	Cooperman et al.			
	5,487,061	1-1996	Bray			
	5,499,238	3-1996	Shon			
	5,757,771	5-1998	Li et al.			
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	5,619,500	4-1997	Hie kali			
	5,768,257	6-1998	Khacherian et al.			Technology Center 2600
	5,790,522	8-1998	Fichou et al.			
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## FOREIGN PATENT DOCUMENTS

## TRANSLATION

DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	YES	NO
00/38380	6-2000	WO				

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, etc.)

Mirfakhraei, "Wafer-Scale Integration as a Technology Choice for High Speed ATM Switching Systems", Proceedings of the Annual International Conference on Wafer Scale Integration (from 1996 Proceedings of the Int'l. Conference on Innovative Systems in Silicon), US, New York, IEEE, vol. CONF. 6, 1994, pp. 282-291
Doi et al, "A High-Speed ATM Switch Architecture for FTTH - An ATM Switch Architecture with Input and Cross-Point Buffers", Proceedings of the International Switching Symposium, DE, Berlin, VDE Verlag, vol. SYMP. 15, 23 April 1995, pp. 384-388

\*Examiner RD Date Considered 30 Sept 04

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.